

FIG. 1

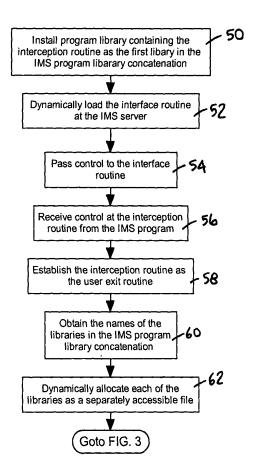


FIG. 2

Operating Logic

